UNITED STATES PATENT AND TRADEMARK OFFICE

application of:

Vishnu, Meenarachagan

pplication No.: 09/503,673

Group No.: 2665

Filed: February 14, 2000

Examiner: Thien D. Tran

For: METHOD AND APPARATUS FOR DYNAMIC BITMAP GENERATOR SCHEDULER

Commissioner for Patents

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APR 0 8 2004

Technology Center 2600

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 - (1) a final action under § 1.113 or
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Fee due \$180.00

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4. Attached is a check in the amount of \$180.00.

A duplicate of this paper is attached.

Ansel M. Schwartz Registration No. 30,587 Attorney at Law 201 N. Craig Street Suite 304 Pittsburgh, PA 15213 412-621-9222

PTO/SB/08a (08-03)

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nder the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known bstitute for form 1449A/PTO **Application Number** 09/503,673 Filing Date INFORMATION DISCLOSURE 02/14/2000 First Named Inventor STATEMENT BY APPLICANT Meenarachagan Vishnu Art Unit 2665 **Examiner Name** Thien D. Tran (Use as many sheets as necessary) Attorney Docket Number FORE-54 Sheet

				U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (# known)}		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		^{US-} 5,850,399	Α	12/15/1998	Yang Tao et al.	
-		^{US-} 5,561,663	Α	10/01/1996	Klausmeier Daniel E.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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	Complete if Know	n
Application Number	09/503,673	RECEIVED
Filing Date	02/14/2000	- TEOFIVED
First Named Inventor	Meenarachagan	VishnuAPR 0 8 2004
Art Unit	2665	Technology
Examiner Name	Thien D. Tran	Technology Center 260
Attorney Docket Number	EODE-54	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	1	HUI HUANG ET AL., "Hierarchical scheduling with adaptive weights for W-ATM," IEEE ATM WORKSHOP. PROCEEDINGS, XX, XX, p. 337-342,	
	V	CHIUSSI F.M. ET AL., "Implementing fair queueing in ATM switches: the discrete-rate approach," INFOCOM '98 SEVENTEENTH ANNUAL JOINT CONFERENCE OF THE IEEE COMPUTER AND COMMUNICATIONS SOCIETIES. PROCEEDINGS. IEEE SAN FRANCISCO, CA USA 29 MARCH-2 APRIL 1998, p. 272-281.	•
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